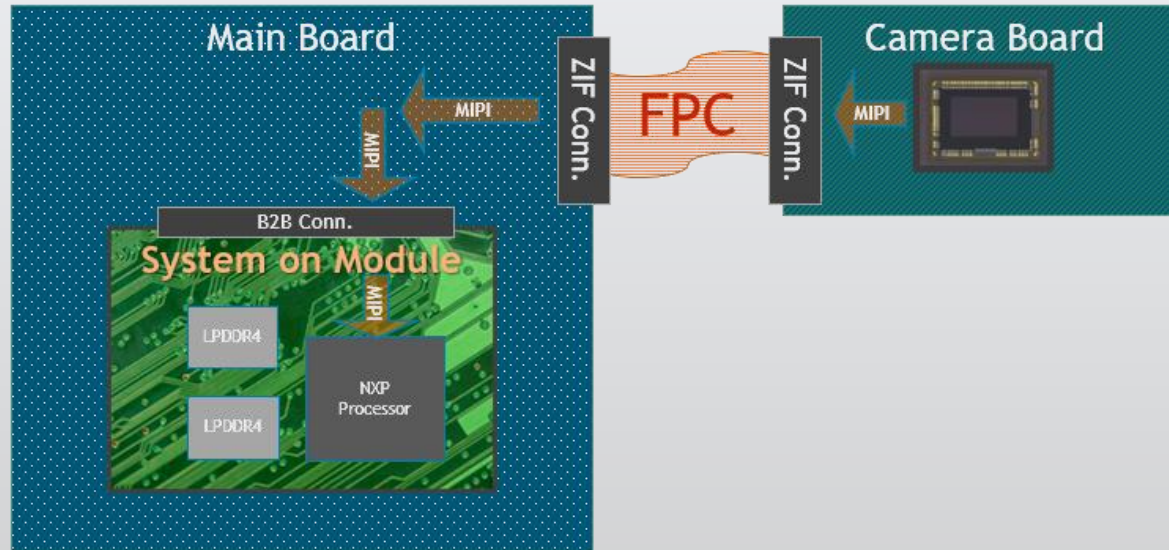


TIEplus 2021 – Subject brief

- The diagram below describes the simplified architecture of a system that incorporates a camera interface connected to a video processing unit, implemented using an NXP processor.



- Your task is to ensure the signal integrity of the camera video interface based on a dedicated MIPI CSI-2 camera link specification.
- Required skills: s-parameter (touchstone file) analysis and processing; loss budget planning; full-wave solver extraction; transient simulation; IO modeling; crosstalk analysis; eye-diagram analysis; stack-up design and material selection; component selection for high speed signaling.